

Applicants respectfully request that the Examiner consider and allow this application.

If there are any additional fees due in connection with the filing of this Preliminary Amendment, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

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APPENDIX TO PRELIMINARY AMENDMENT OF JANUARY 23, 2002

Version with Markings to Show Changes Made

Amendments to the Specification

FIG. 2, FIG. 3a and FIG. 3b are views showing a first embodiment according to the present invention. FIG. 3a and FIG. 3b are respectively sectional views taken along line B-B' and line C-C' of the FIG. 2. As shown in these figures, gate and data bus lines 101 and 102 are formed on a first substrate 110, defining a pixel. Although only one pixel is drawn in these figures, a liquid crystal display device generally has a plurality of pixels. In the pixel, a common bus line 103 is formed parallel to the data bus line [102] 101. At the cross of the gate and data bus lines 101 and 102, a thin film transistor (TFT) is formed. In the pixel, data and common electrodes 108 and 109 are disposed parallel to data bus line 102. As in the conventional IPS mode LCD, data electrode 108 has a portion overlapping common bus line 103 for obtaining a first storage capacitor (C_{s11}) as shown in FIG. 3b. In addition, common electrode 109 has a portion overlapping data electrode 108 for obtaining a second storage capacitor (C_{s12}). Common electrode 109 is connected to common bus line 103 through a hole 125.